

WEST Search History

DATE: Wednesday, September 29, 2004

09711 866
[Signature]

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
<input type="checkbox"/>	L14	L13 and @pd > 20040420	0
<input type="checkbox"/>	L13	('4586178')!.PN.	2
<input type="checkbox"/>	L12	L11 and (test\$3) same (cell) with compar\$7 near2 (PARALLEL\$4 OR CONCUR\$7 OR SIMULTANE\$7 OR CONTEMPORan\$7 or CONTEMPO\$7 or contemporaneously) with (location or address)	5
<input type="checkbox"/>	L11	L10 and (test\$3) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array or cell) with compar\$7 near2 (PARALLEL\$4 OR CONCUR\$7 OR SIMULTANE\$7 OR CONTEMPORan\$7 or CONTEMPO\$7 or contemporaneously) with (location or address)	63
<input type="checkbox"/>	L10	L9 and compar\$7 near2 (PARALLEL\$4 OR CONCUR\$7 OR SIMULTANE\$7 OR CONTEMPORan\$7 or CONTEMPO\$7 or contemporaneously) with (location or address)	311
<input type="checkbox"/>	L9	L8 and compar\$7 near2 (PARALLEL\$4 OR CONCUR\$7 OR SIMULTANE\$7 OR CONTEMPORan\$7 or CONTEMPO\$7 or contemporaneously)	366
<input type="checkbox"/>	L8	L7 and compar\$7 with (location or address) with (PARALLEL\$4 OR CONCUR\$7 OR SIMULTANE\$7 OR CONTEMPORan\$7 or CONTEMPO\$7 or contemporaneously)	891
<input type="checkbox"/>	L7	((test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array or cell) with compar\$7 with (location or address))	10927
<input type="checkbox"/>	L6	((('6202180' '6574763')!.PN.) and @pd > 20040111	0
<input type="checkbox"/>	L5	(L2 and fus\$3) and @pd > 20040111	0
<input type="checkbox"/>	L4	((('4499556' '5325365')!.PN.) and @pd > 20040111	0
<input type="checkbox"/>	L3	((('6202180')!.PN.) and @pd > 20040111	0
<input type="checkbox"/>	L2	(L1 and (substitut\$3 or replac\$7 or repair\$4 or spare) with (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) with (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array or cell) with compar\$7 with (location or address) same reference) and @pd > 20040111	0
<input type="checkbox"/>	L1	((test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array or cell) with compar\$7 with (location or address)) and @pd > 20040111	848

END OF SEARCH HISTORY

h e b b cg b chh e f f e g e